

6 **METHOD AND APPARATUS FOR PREDICTIVE FLASH**
7 **MEMORY ERASE AND WRITE TIMES**
8

9 **ABSTRACT**

10 A NOR-gate architecture memory with an erase and write time table. The memory
11 processor creates an erase and write time table in a table block. The table contains the most
12 recent times for erase and write operations for each data block in the memory. When a
13 storage operation is initiated, the processor accesses the table and estimates the amount of
14 time it will take to perform the data storage operation and then communicates that back to a
15 host computer. Each data storage operation results in a new table being created that is written
16 into the data block. To save erase and write operations for the table block, the new table is
17 written directly after the most recent table unless there is not enough space. An erase and
18 write operation is only performed on the table block if there is not enough space for the new
19 table.